

#5/A G.L.
5/8/02

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application Serial No. 09/921,518
 Filing Date August 1, 2001
 Inventor John T. Moore
 Assignee Micron Technology, Inc.
 Group Art Unit 2818
 Examiner Phuc T. Dang
 Attorney's Docket No. MI22-1669
 Title: Method of Forming Integrated Circuitry, Method of Forming Memory Circuitry, and Method of Forming Non-Volatile Random Access Memory Circuitry

**RESPONSE TO MARCH 29, 2002 OFFICE ACTION**

To: Box Non-Fee Amendment
 Assistant Commissioner for Patents
 Washington, D.C. 20231

VIA U.S. EXPRESS MAIL

From: Mark S. Matkin (Tel. 509-624-4276; Fax 509-838-3424)
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Responsive to the Office Action dated March 29, 2002, Applicant amends and remarks as follows:

AMENDMENTS**In the Specification**

Please replace the paragraphs listed below with the following clean replacement paragraphs in accordance with 37 CFR § 1.121(b)(1)(ii). A marked-up version showing amendments to the specification paragraphs being changed is provided in one or more accompanying pages separate from this amendment in accordance with 37 CFR § 1.121(b)(1)(iii).

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